

WHAT IS CLAIMED IS:

1. An apparatus comprising:

a pre-formed portion of underfill material defining openings, the openings to pass electrical interconnects for coupling an integrated circuit die to a portion of a substrate.

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2. An apparatus according to Claim 1, further comprising:

a second pre-formed portion of underfill material coupled to the portion of underfill material, the second portion of underfill material defining second openings, the second openings to pass second electrical interconnects for coupling a second integrated circuit die to a second portion of a substrate.

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3. An apparatus according to Claim 2, further comprising a pre-formed sheet of underfill material comprising the first portion and the second portion.

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4. An apparatus according to Claim 2, further comprising a pre-formed tape of underfill material comprising the first portion and the second portion.

5. An apparatus according to Claim 1, further comprising:

the portion of the substrate.

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6. An apparatus according to Claim 5, further comprising:

the integrated circuit die.

7. An apparatus according to Claim 1, further comprising:

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the integrated circuit die.

8. An apparatus according to Claim 1, the underfill material comprising:
no-flow underfill material.

5 9. A method comprising:

 manufacturing a pre-formed portion of underfill material defining openings, the
openings to pass electrical interconnects for coupling an integrated circuit die to a portion of
a substrate.

10 10. A method according to Claim 9, wherein manufacturing the portion comprises:

 pressing the underfill material against a template of the openings to create the
openings.

 11. A method according to Claim 10, wherein manufacturing the portion further
15 comprises:

 plasma etching the openings to further create the openings.

 12. A method according to Claim 9, further comprising:

 attaching the underfill material to the substrate.

20 13. A method according to Claim 12, further comprising:

 coupling the substrate to the integrated circuit die using electrical interconnects
passing through the openings.

25 14. A system comprising:

a microprocessor comprising:

an integrated circuit die;

a substrate; and

5 a pre-formed portion of underfill material pre-formed to define openings, the openings passing electrical interconnects for coupling the integrated circuit die to the substrate; and

a double data rate memory coupled to the microprocessor.

15. A system according to Claim 14, the underfill material comprising:

10 no-flow underfill material.

16. A system according to Claim 14,

the integrated circuit die comprising a first plurality of electrical contacts;

the substrate comprising a second plurality of electrical contacts; and

15 the electrical interconnects for coupling the first plurality of electrical contacts to the second plurality of electrical contacts.